IN THE ABSTRACT:

Please amend the abstract as follows:

At page 38 of the application, first paragraph, line 2, please delete the following paragraph:

A number of service DSP engines form an array or matrix wherein the service DSP engines are coupled to a master DSP engine using a channelized serial bus. The master DSP engine controls a memory comprising a number of firmware algorithms used in processing a number of types of data. The master DSP engine continuously broadcasts the firmware algorithms to the service DSP engines over the channelized serial bus. The DSP array receives PCM data from multiplexed lines of a public switched telephone network and packetized data from an Internet Protocol (IP) network. The data may include, but is not limited to, modem data, voice data, audio data, video data, and facsimile data. The data is provided to one of a number of service DSP engines of a DSP array. Upon receipt of the data, each service DSP engine determines the type of the received data and determines a firmware algorithm required to process that type of data. The service DSP engine then determines an address of at least one channel of the channelized serial bus on which the required firmware algorithm is available and unmasks a corresponding bit of an interrupt mask in the service DSP engine. In response to receipt of an interrupt signal corresponding to the unmasked

interrupt bit, the service DSP engine executes an interrupt service routine resulting in the receipt and storage of the corresponding firmware algorithm from the master DSP engine. The service DSP engine processes the received data using the received firmware algorithm. When the data received by the service DSP engine is PCM data received from the PSTN, the service DSP engine produces packetized data for communication over the IP network. When the data received by the service DSP engine is packetized data received from the IP network, the service DSP engine produces PCM data for communication over the PSTN.

At page 38 of the application, first paragraph, line 2, please insert the following paragraph:

A system and method for making available on a dynamic basis a large library of different firmware processing algorithms to each DSP processor engine of a DSP processor array is disclosed. A master DSP engine continuously broadcasts firmware algorithms used in processing a number of types of data from an attached memory to service DSP engines over the channelized serial bus. The DSP array receives PCM data from multiplexed lines of a public switched telephone network and packetized data from an Internet Protocol (IP) network, then each service DSP engine determines a firmware algorithm required to